

2183

PTO/SB/21 (08-00)

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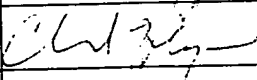
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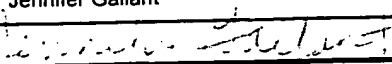
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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/802,017
	Filing Date	March 8, 2001
	First Named Inventor	Ashley Saulsbury
	Group Art Unit	
	Examiner Name	
Total Number of Pages in This Submission	Attorney Docket Number	16747-009910US

ENCLOSURES (check all that apply)		
<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment / Response <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Third Information Disclosure Statement Under 37 CFR §1.97 and §1.98 <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Assignment Papers (for an Application) <input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition Routing Slip (PTO/SB/69) and Accompanying Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s)	<input type="checkbox"/> After Allowance Communication to Group <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Return Postcard <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): Copies of five (5) cited references
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Date	12/11/01

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PATENT
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Ashley Saulsbury, et al.

Application No.: 09/802,017

Filed: March 8, 2001

For: VLIW Computer Processing
Architecture with On-chip DRAM Usable
as Physical Memory or Cache Memory

Examiner: Unassigned

Art Unit: 2183

THIRD INFORMATION DISCLOSURE
STATEMENT UNDER 37 CFR §1.97 and
§1.98

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

The references cited on attached form PTO/SB/08A and PTO/SB/08B are being called to the attention of the Examiner. Copies of the references are enclosed. Also enclosed is a copy of the Search/Examination report corresponding to the PCT application. It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no

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Substitute for form 1449B/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet

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of

Complete if Known

Application Number	09/802,017
Filing Date	March 8, 2001
First Named Inventor	Saulsbury, Ashley
Group Art Unit	
Examiner Name	
Attorney Docket Number	016747-009910US

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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FOREIGN PATENT DOCUMENTS

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OTHER PRIOR ART -- NON-PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	A	NUMOMURA Y ET AL: "M32R/D-INTEGRATING DRAM AND MICROPROCESSOR" IEEE MICRO, IEEE INC. NEW YORK, US, vol. 17 no. 6, 1 November 1997 (1997-11-01), pages 40-48, XP000726003; ISSN: 0272-1732.	
	A	KOZYRAKIS C E ET AL: "SCALABLE PROCESSORS IN THE BILLION-TRANSISTOR ERA: IRAM" COMPUTER, IEEE COMPUTER SOCIETY, LONG BEACH, CA, US, US vol. 20, no. 0 1 September 1997 (1997-09-01), pages 75-78, XP000730003; ISSN: 0018-9162.	
	C	HERRMANN KLAUS, HILGENSTOCK JOERG, PIRSCH PETER: "ARCHITECTURE OF A MULTIPROCESSOR SYSTEM WITH EMBEDDED DRAM FOR LARGE AREA INTEGRATION" 8 October 1997 (1997-10-08), IEEE INTERNATIONAL CONFERENCE ON INNOVATIVE SYSTEMS IN SILICON, PISCATAWAY, NJ, USA; XP002179990	

Examiner
Signature

Date
Considered

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.

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